

Robert D. “Skip” Rung

President and Executive Director

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Mr. Rung is the founder (2003), President, and Executive Director of ONAMI, Oregon’s first “Signature Research Center” and an unprecedented collaboration among Oregon’s research universities and the Pacific Northwest National Laboratory. ONAMI’s dual mission is to grow “small tech” research in Oregon and commercialize technology in order to extend the success of Oregon’s world-leading “Silicon Forest” technology cluster. ONAMI has so far received \$47M in state investment and approximately quadrupled (from \$9M/yr. to ~\$40M/yr.) Oregon’s annual research volume in the fields of green nanotechnology, nanostructured materials, nanoscale metrology, and microtechnology-based energy and chemical systems. ONAMI’s commercialization “gap” fund has made \$4.6M in grant disbursements (as of March 2012), enabling over 25 portfolio companies to raise \$103M (\$10M federal grants, \$93M private capital) in leveraged funding. ONAMI is currently assisting the new NSF Center for Sustainable Materials Chemistry with a unique innovation program focused on both graduate student career planning and technology commercialization. Finally, ONAMI’s NWNanoNet™ network of shared user facilities at three public universities supports research, industry, and startup company clients with easy access to electron microscopy, materials characterization, and device prototyping services.

From 1987 to 2001, Mr. Rung was Director of Advanced Research and Development at Hewlett-Packard’s Corvallis, OR facility, responsible for multiple generations of HP’s world-leading inkjet printing technology. Prior to this, Mr. Rung was the R&D Manager for HP’s Northwest Integrated Circuits Division in Corvallis, which achieved worldwide ASIC technology leadership in 1986 with a 1-micron process comparable to those used for DRAM. Mr. Rung’s organization also developed novel and performance-leading in-house IC design automation systems and custom IC packaging technologies (hybrids, flat packs, TAB) to enable calculators and other HP products.

Mr. Rung began his industrial career in 1977 at Hewlett-Packard Laboratories in Palo Alto, CA, performing advanced research related to CMOS process integration. In 1981-1982, Mr. Rung was selected by HP to be a technology exchange engineer with Toshiba Corp. in Kawasaki, Japan. Several publications related to device isolation and CMOS latch-up resulted from this collaboration.

Mr. Rung received his BSEE and MSEE co-terminally in 1976 from Stanford University, where he was elected to both Phi Beta Kappa and Tau Beta Pi in his junior year.

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