

Bio:

Ji Ung Lee is the Empire Innovation Professor of Nanoscale Engineering at the College of Nanoscale Science and Engineering of the University at Albany – SUNY, a position he took in early 2007. He received his Ph.D. in Electrical Engineering from the University of Wisconsin-Madison (1996). Previously, he was at Argonne National Laboratory from 1996-1997 as a post-doctoral scientist and at the General Electric Global Research Center from 1999-2007 as a Senior Scientist. His current interest is in the design and characterization of carbon nanotube and graphene electronic and optical devices. For his work on carbon nanotubes electronics, he was awarded the 2004 GE Global Research Hull Award, the highest individual honor for young scientists and the 2005 NASA Nanotech Briefs Nano50 Technology Award. More recently, he was awarded the 2007 IBM Faculty Development Award. In addition to his academic duties, he coordinates the INDEX program, a multi-university program with funding from NRI/SRC to investigate non-charge based logic switches for post CMOS electronics. He is also a voting member of the SEMATECH Front-End Program Advisory Group. He holds 21 issued patents, has given over 25 invited talks in the area of carbon nanotube electronics and has 20 published articles.