

Prashant Majhi

Bio:

Prashant Majhi received his Bachelors of Technology degree from the Indian Institute of Technology, Madras (1996), and his Ph.D. degree in science and engineering of materials program from Arizona State University, Tempe, AZ (2000). He joined Philips Semiconductor in The Netherlands and worked on several CMOS and mixed-signal process technologies. In 2004, he joined Intel Corp., and is currently at SEMATECH as an Intel Assignee and Program manager for advanced CMOS division. His group's primary research activities span scaled MOSFETs in planar and non-planar forms, focused on high-k/ metal gate process and devices, advanced junctions and contacts, high mobility channels (group IV and III-V), and steep-sub-threshold tunneling based FETs. He has authored and co-authored more than 250 journal and conference papers in the various semiconductor research areas.