Subramanian S. Iyer (Subu) is Distinguished Professor and holds the Charles P. Reames Endowed Chair in the Electrical Engineering Department and a joint appointment in the Materials Science and Engineering Department at the University of California at Los Angeles. He is Director of the Center for Heterogeneous Integration and Performance Scaling (UCLA <u>CHIPS</u>). Prior to that he was an IBM Fellow. His key technical contributions have been the development of the world's first SiGe base HBT, Salicide, electrical fuses, embedded DRAM and 45nm technology node used to make the first generation of truly low power portable devices as well as the first commercial interposer and 3D integrated products. He has been exploring new packaging paradigms and device innovations that may enable wafer-scale architectures, in-memory analog compute and medical engineering applications. He is a fellow of IEEE, APS, iMAPS and NAI as well as a Distinguished Lecturer of IEEE EDS and EPS. He is on the Board of Governors of IEEE EPS. He is a Distinguished Alumnus of IIT Bombay and received the IEEE Daniel Noble Medal for emerging technologies in 2012 and the 2020 iMAPS Daniel C. Hughes Jr Memorial award and the iMAPS distinguished educator award in 2021. Prof. Iyer is also Prof. Ramakrishna Rao Visiting Chair Professor at IISc, Bengaluru.

List of publications/patents:

https://scholar.google.com/citations?user=xXV4oIMAAAAJ&hl=en

Picture: (if needed)



Subramanian S. Iyer Email: <u>S.S.Iyer@ucla.edu</u> Website : <u>www.chips.ucla.edu</u>

Distinguished Professor Charles P. Reames Endowed Chair Electrical Engineering Department Material Science and Engineering Department Henry Samueli School of Engineering and Applied Science UCLA 420 Westwood Plaza 66-147H Engineering IV Los Angeles , CA 90024 Tel: <u>310 825 6913</u> (w) <u>914 329 3341</u> (m)