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Gill Lee is Managing Director of Technical Marketing and Strategic Programs for Korea region in the Semiconductor Product Group at Applied Materials, Inc. He joined Applied Materials in March 2009 and worked in various positions as the Integrated Materials System Memory Technology head, CTO of Patterning & Packaging Group, Memory TD in the Silicon Systems Group. Gill has previously worked on various memory technology developments at Qimonda/Infineon/Siemens in NY, France, and Germany. He earned his B.S. in Ceramic Engineering in 1988 from Yonsei University and his M.S. in Materials Science and Engineering in 1991 from POSTECH. He has authored a number of patents and papers on CMOS integration and process developments.