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Jae-sun Seo received his Ph.D. degree from the University of Michigan in 2010 in electrical engineering. From 2010 to 2013, he was with IBM T. J. Watson Research Center, where he worked on energy-efficient circuits for high-performance processors and neuromorphic chip design for the DARPA SyNAPSE project. In January 2014, he joined Arizona State University as an assistant professor in the School of ECEE. During the summer of 2015, he was a visiting faculty at Intel Circuit Research Labs. His research interests include efficient hardware design of learning algorithms and integrated power management. He received the IBM outstanding technical achievement award in 2012, and serves on the technical program committee for ISLPED, ISOCC, and organizing committee for ICCD.