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Changhwan Choi is an associate professor at division of materials science and engineering at Hanyang University, Seoul, Republic of Korea. He received B.S and M.S degrees in materials science and engineering from Hanyang University and The University of Texas at Austin, respectively. He also attained Ph.D degree in electrical and computer engineering from The University of Texas at Austin.

Before joining into school, he had worked for IBM Thomas J. Watson Research Center, Yorktown Heights, NY, USA, as a research staff member, where he did research on semiconductor material and device fabrication processes, characterization and modeling for 45-, 32- and 22 nm technology.

His main interests are nanoelectronic devices, materials and process developments for advanced memory and logic device applications.