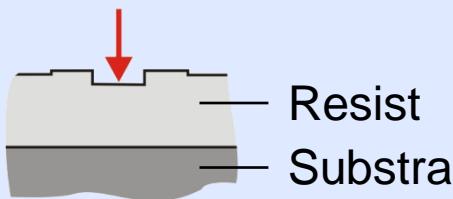
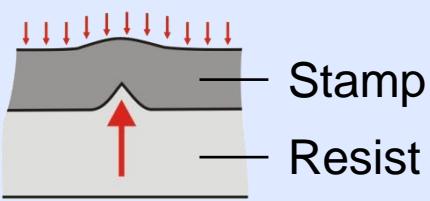


Fast nanoimprint lithography simulation for process design · Hayden Taylor, UC Berkeley

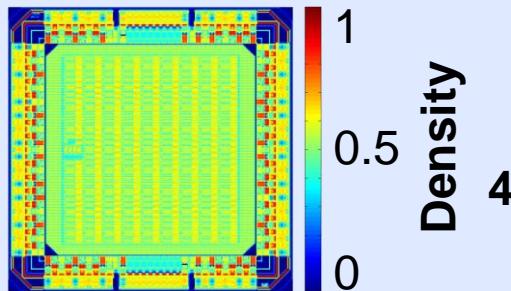
Resist surface's impulse response



Stamp's load response (bending, indentation) 2



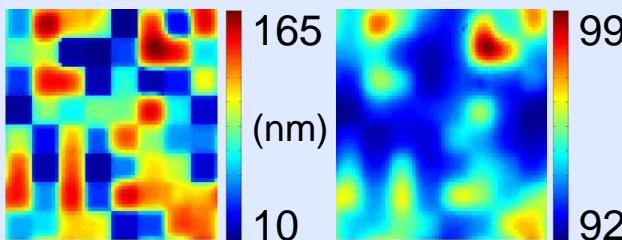
Pattern abstraction 3



Example questions:

Does changing stamp material affect residual layer uniformity? ^{1,2}

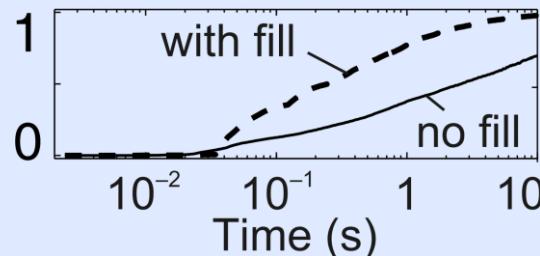
Elastomer



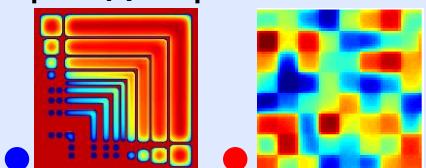
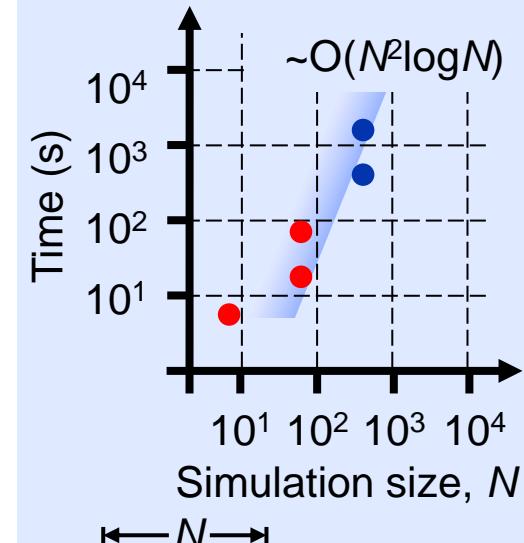
Silicon

Can 'dummy fill' accelerate stamp cavity filling? ³

Proportion cavities filled



Simulations need to be highly scalable



- At least 10^3 times faster than FEM
- Can balance spatial resolution and speed

¹ Taylor *NNT* 2009, 2011; ² Taylor *SPIE* 7641 2010; ³ Boning *et al.* *NNT* 2010