Biography:

Shimeng Yu received the B.S. degree in microelectronics from Peking University, Beijing, China in 2009, and the M.S. degree and Ph.D. degree in electrical engineering from Stanford University, Stanford, CA, USA in 2011, and in 2013, respectively. He is currently an assistant professor of electrical engineering and computer engineering at Arizona State University, Tempe, AZ, USA.

His research interests are emerging nano-devices and circuits with a focus on the resistive memories for different applications including monolithic 3D integration, brain-inspired neuromorphic computing, hardware security, radiation-hard electronics, etc. He has published >50 journal papers and >90 conference papers with citations >3000 and H-index 26.

Among this honors, he is a recipient of the Stanford Graduate Fellowship from 2009 to 2012, the IEEE Electron Devices Society Masters Student Fellowship in 2010, the IEEE Electron Devices Society PhD Student Fellowship in 2012, the DOD-DTRA Young Investigator Award in 2015, and the NSF CAREER Award in 2016.